ar Docket No: 42P13230D IN THE UNITED STATES PATENT AND TRADEMARK OFFICE fe Application of: Shafquat Ahmed et al. Examiner: Booth, Richard Serial No: 10/619,961 Art Unit: 2812 Filed: July 15, 2003 For: Method of Forming Transistor Having) Insulating Spacers On Gate Sidewalls_) **RESPONSE TO OFFICE ACTION** Mail Stop Amendment

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

In response to the Office Action mailed June 29, 2004, the Applicants respectfully request that the Examiner enter the following amendments and consider the following remarks.

FIRST CLASS CERTIFICATE OF MAILING

I hereby certify that I am causing the above-referenced correspondence to be deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and that this paper or fee has been addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450

October 29, 2004

Date of Deposit

Krista Mathieson

Name of Person Mailing Correspondence

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